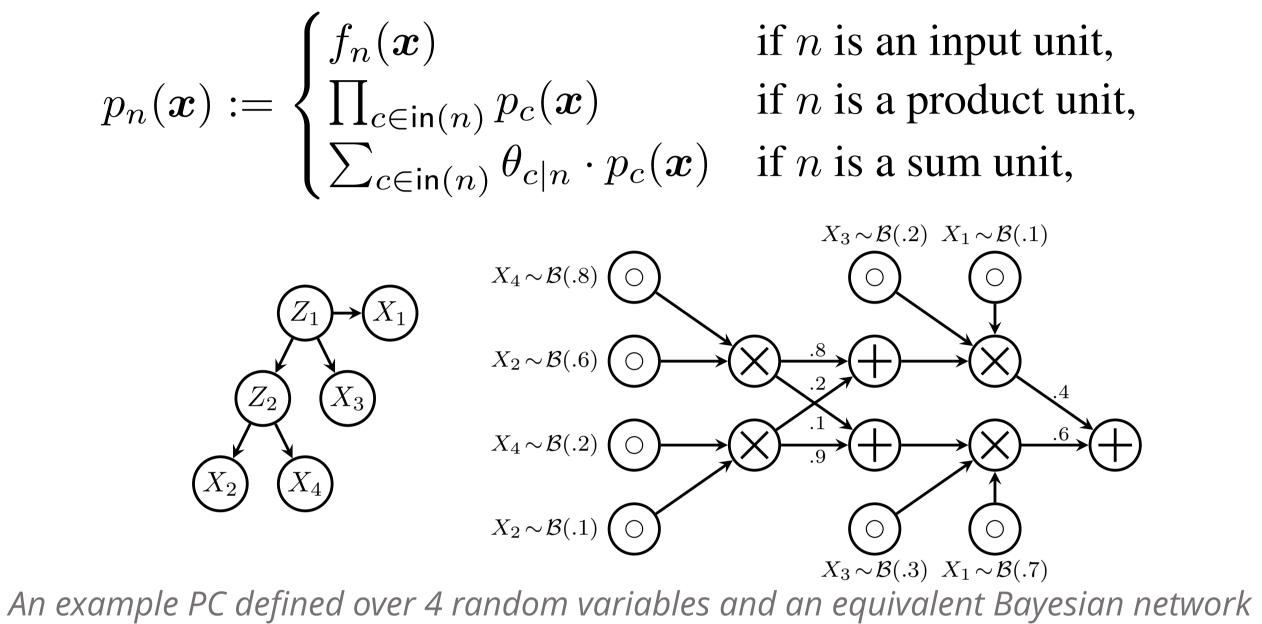
Guy Van den Broeck Anji Liu Meihua Dang University of California, Los Angeles

Probabilistic Circuits

Probabilistic circuits (PCs) encode a probability distribution $p_n(x)$ defined recursively as follows.



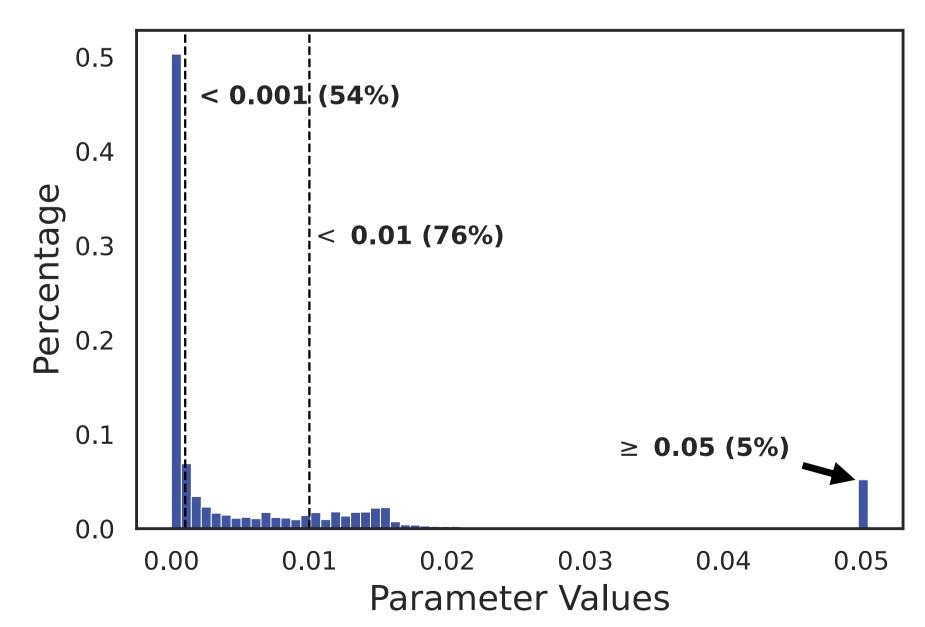
We study *smooth* and *decomposable* PCs.

Two perspectives:

- *Computational graph*: inference as forward propagation.
- 2. *Probability semantics*: parameter value represents local conditional probability.

Motivation: Fully-connected Layers are Sparsely Used

As we scale up learning PCs, the performance of PCs plateaus as model size increases. Thus, we need to better utilize the available capacity.



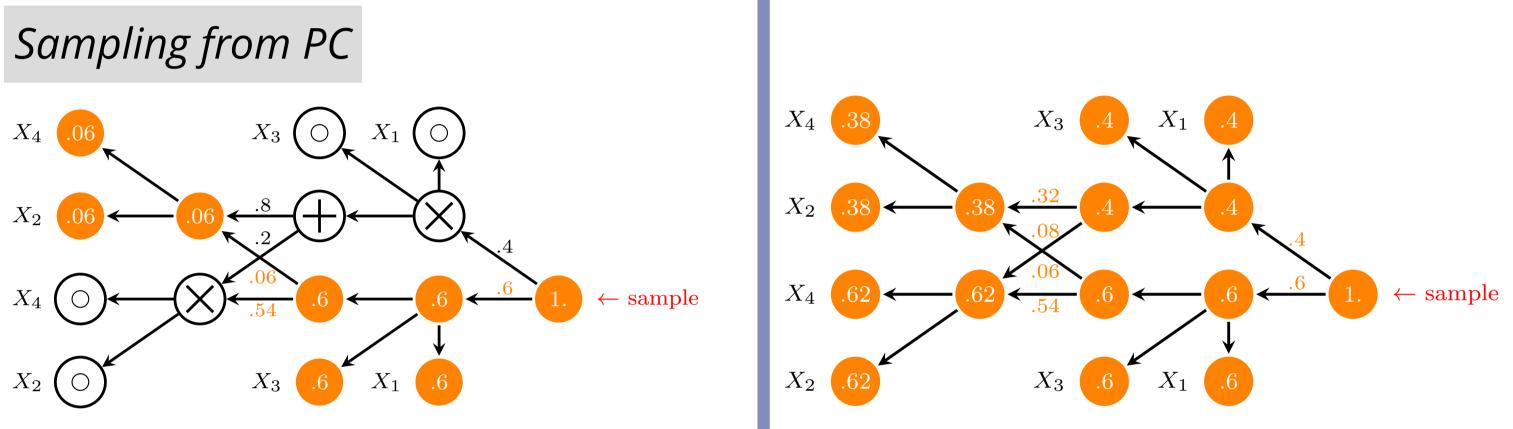
Histograms of a SoTA PC on MNIST, 95% of the parameters have close-to-zero values

Though PC structures have fully-connected parameter layers, the parameter values are only sparsely used.

Methods: Pruning Parameters by Probability Semantics

By pruning away "unimportant" paraneters, it is possible to significantly reduce model size while maximally retaining model expressiveness.

Intuition: when drawing a sample from PC, if a parameter is seldom visited in the generative sampling process, removing it will not significantly affect the PC's distribution.



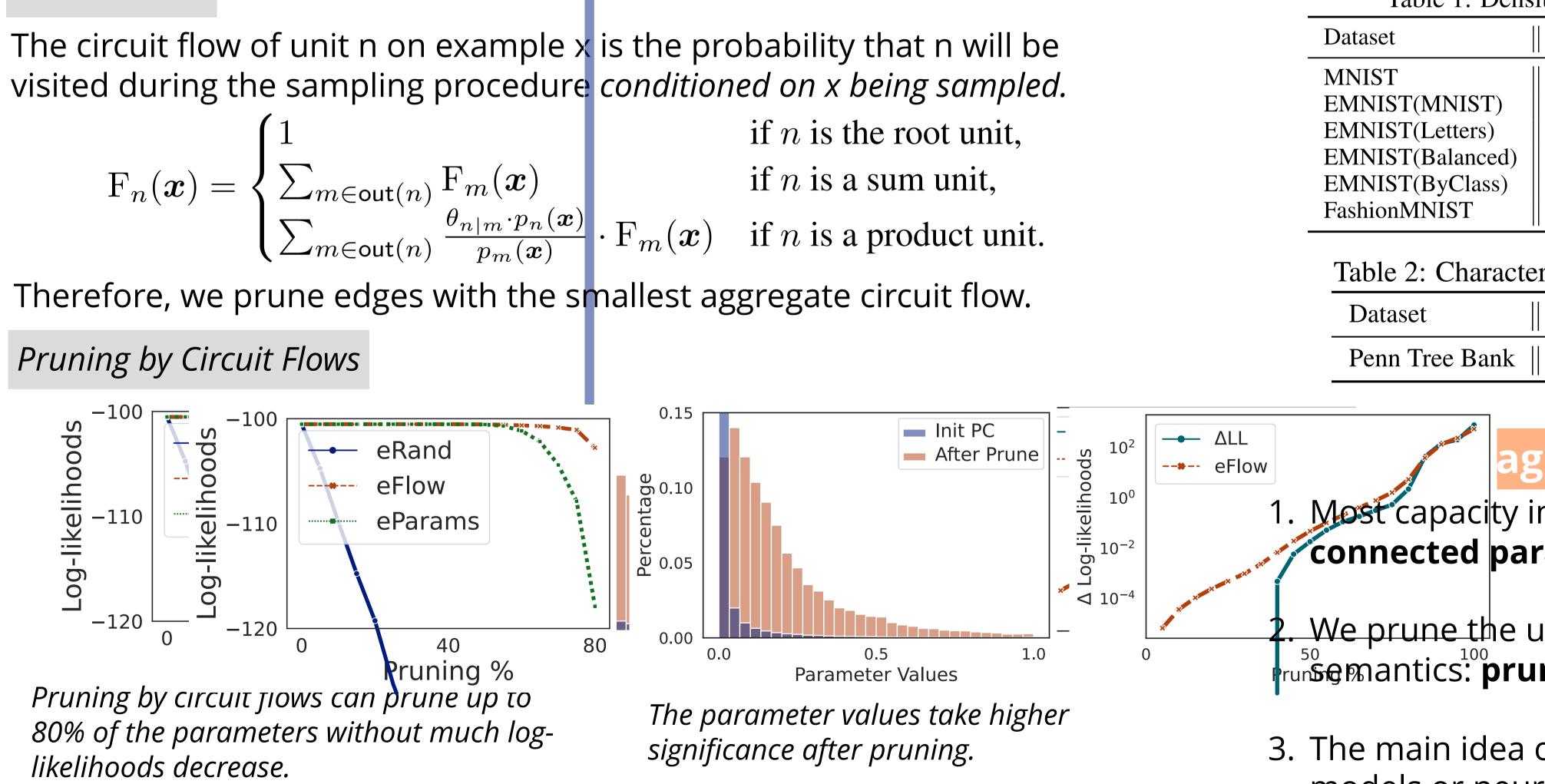
Sampling as a backward propagation

Circuit Flows

-120

$$F_n(\boldsymbol{x}) = \begin{cases} 1\\ \sum_{m \in \mathsf{out}(n)} F_m(\boldsymbol{x})\\ \sum_{m \in \mathsf{out}(n)} \frac{\theta_{n|m} \cdot p_n(\boldsymbol{x})}{p_m(\boldsymbol{x})} \end{cases}$$

Pruning by Circuit Flows



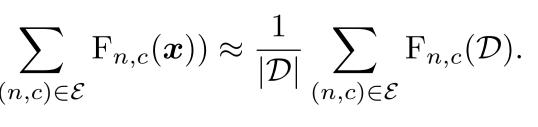
Pruning by circuit jiows can prune up to 80% of the parameters without much loglikelihoods decrease.

(Theorem) The log-likelihood drop by pruning away multiple edges is bounded and approximated by

$$\Delta \mathcal{LL}(\mathcal{D}, \mathcal{C}, \mathcal{E}) \leq -\frac{1}{|\mathcal{D}|} \sum_{\boldsymbol{x}} \log(1 - \sum_{n \in \mathcal{A}} \log(1 - \sum_{n \in \mathcal{A}$$

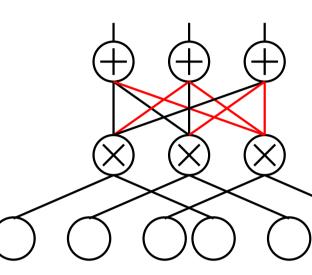


The probability of each unit being sampled





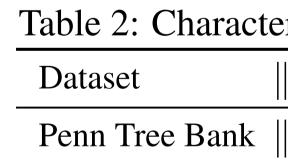
Learning Sparse PCs



(a) PC with fully connected layers

Density Estimation Benchmarks

Table 1: Density estimation performance on MNIST-family datasets in test set bpd.									
Dataset	Sparse PC (ours)	HCLT	RatSPN	IDF	BitSwap	BB-ANS	McBits		
MNIST	1.14	1.20	1.67	1.90	1.27	1.39	1.98		
EMNIST(MNIST)	1.52	1.77	2.56	2.07	1.88	2.04	2.19		
EMNIST(Letters)	1.58	1.80	2.73	1.95	1.84	2.26	3.12		
EMNIST(Balanced)	1.60	1.82	2.78	2.15	1.96	2.23	2.88		
EMNIST(ByClass)	1.54	1.85	2.72	1.98	1.87	2.23	3.14		
FashionMNIST	3.27	3.34	4.29	3.47	3.28	3.66	3.72		

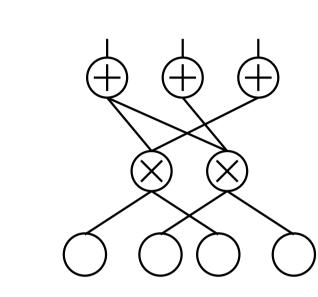


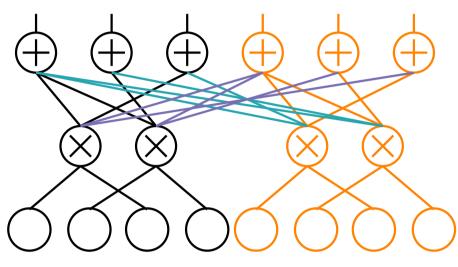
models or neural networks.

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Learning Sparse PCs Structures

1. Growing operation copies parameters and injects noise 2. Apply pruning, growing, EM iteratively to learn structures





(c) PC after growing operation

(b) PC after pruning operation

Image datasets: MNIST, EMNIST, FashionMNIST Character-level language modeling task: PTB Baselines: PC learners (HCLT, RatSPN), VAEs, flow-based models

|--|

Sparse PC (ours)	Bipartite flow [42]	AF/SCF [48]	IAF/SCF [48]
1.35	1.38	1.46	1.63

. Most capacity in existing large PC structures is wasted: **fully**connected parameter layers are only sparsely used.

We prune the unimportant parameters based on their probability ករកនិទ្ធគ្នាantics: pruning away low probability sub-structures.

3. The main idea can be generalized to compress other deep generative

Scan this code \rightarrow

